
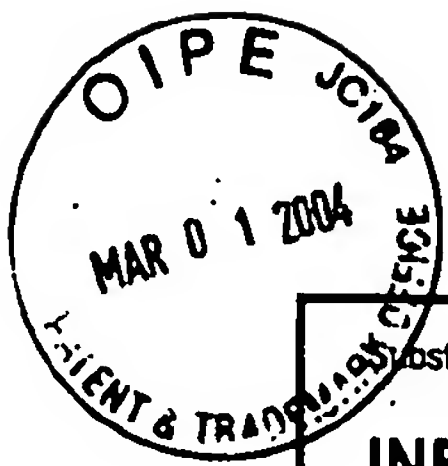


ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	PROGRAMMABLE SLEW RATE CONTROL FOR DIFFERENTIAL OUTPUT						
Application Number: 10/708303							
Confirmation Number: 2302							
First Named Applicant: Wilson Wong							
Attorney Docket Number: 015114-068500US							
Search string: (6242941 or 6184703 or 5066873 or 6154059 or 6246270 or 5958026 or 6184708).pn.							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	6242941	2001-06-05	Vest et al.			
<input type="checkbox"/>	2	6184703	2001-02-06	Vest et al.			
<input type="checkbox"/>	3	5066873	1991-11-19	Chan et al.			
<input type="checkbox"/>	4	6154059	2000-11-28	Cheung et al.			
<input type="checkbox"/>	5	6246270	2001-06-12	Wang et al.			
<input checked="" type="checkbox"/>	6	5958026	1999-09-28	Goetting et al.			
<input checked="" type="checkbox"/>	7	6184708	2001-02-06	Jenkins, IV			
Signature							
Examiner Name				Date			
James Cho				8/17/05			



INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	10/708,303
				Filing Date	February 24, 2004
				First Named Inventor	Wong, Wilson
				Art Unit	Unassigned
				Examiner Name	Unassigned
Sheet	1	of	1	Attorney Docket Number	015114-068500US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>AC</i>	AA	Altera Data Sheet, "Max 7000: Programmable Logic Device Family," Altera Corp., ver. 6.6, June 2003, pp. 1-21.	
<i>AC</i>	AB	Altera Data Sheet, "Max 9000: Programmable Logic Device Family," Altera Corp., ver. 6.5, June 2003, pp. 1-46.	
<i>AC</i>	AC	Altera Data Sheet, "FLEX 10K: Embedded Programmable Logic Device Family," Altera Corp., ver. 4.2, January 2003, pp. 1-40.	
<i>AC</i>	AD	<u>Cyclone Device Handbook</u> , vol. 1, Chapter 2, "Cyclone Architecture," Altera Corp., CS1002-1.2, October 2003, pp. 2-1 to 2-56.	

Examiner Signature	<i>James Cho</i>	Date Considered	<i>8/17/05</i>
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.